

NOV 22 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No..... 09/292,132
Filing Date..... April 14, 1999
Inventor Salman Akram et al.
Assignee Micron Technology, Inc.
Group Art Unit..... 2812
Examiner Savitri Malpuri
Customer No. 021567
Attorney Docket No..... MI22-1171
Title: Methods of Forming a Transistor Gate

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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1. Certificate of Facsimile Transmission
2. Examiner Interview Summary

Dated: 11/22/05

By:



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FEE DEFICIENCY

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

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Inventor..... Salman Akram et al.
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Title: Methods of Forming a Transistor Gate

EXAMINER INTERVIEW SUMMARY OF NOVEMBER 22, 2005

To: Commissioner for Patents
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Applicant's representative held an interview with Examiner Mulpuri on November 22, 2005. Applicant's representative would like to thank Examiner Mulpuri for her time and attention to this matter.

The pending rejection under 35 U.S. §112, first paragraph, against claims 88, 90 and 99-101 were discussed. No agreement was reached.

Respectfully submitted,

Dated: 11-22-05

By: 
D. Brent Kenady
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